

Notice of Allowability

Application No.

10/735,905

Examiner

Bobbak Safaipoor

Applicant(s)

KOZAKI, MASATO

Art Unit

2618

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/25/2006.
2. ☒ The allowed claim(s) is/are 3,5-7,10 and 12-14.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Notice of Allowability

Claims 3-4, 6-7, 10, and 12-14 are allowed.

The following is an examiner's statement of reasons for allowance:

Consider **claims 3, 5, 10, and 12**, the best prior art of record during the examination of the present application, **Nakagawa et al (US Patent Application Publication #2003/0058183 A1)**, in view of **Ikeda (European Patent Application #EP 1 349 231 A2)**, fails to specifically disclose, teach, or suggest a satellite broadcast reception converter wherein the other end portion of the circuit board and the other end portion of the auxiliary board are connected together via a pin having a "U" shape in longitudinal section view. Furthermore, the best prior art of record fails to specifically disclose, teach, or suggest the circuit board and the auxiliary board are connected together electrically through solder filled in the through hole.

Nakagawa et al disclose a satellite broadcast reception converter comprising: a shield case 5 (chassis) (figures 1 and 2) in which a reflection mirror (primary reflector) (figure 17) into which radio wave signals received from an external parabola antenna, and an output terminal to be connected to an external tuner, are positioned a predetermined distance from each other so that signals fed from the primary reflector are amplified (paragraph 65; The satellite broadcast reception converter receives electric waves transmitted from two adjacent satellites and transmits circularly-polarized wave signals that are converged by the reflection mirror, pass through the waterproof cover and then are incident into the first and second waveguides) and converted into

Art Unit: 2618

intermediate-frequency output signals so as to be fed out through the output terminal (paragraph 72, figure 19; The reception signals detected are converted to IF frequency signals by a converter circuit mounted on the first and second circuit boards and then output therefrom. The converter circuit comprises a frequency amplifying circuit portion 104 for amplifying the signals output from the frequency convert 103.),

a first circuit board (paragraph 58; figure 10) of which another end portion thereof (read as waterproof cover) (paragraph 64; figure 1, which is a cross-sectional view showing the satellite broadcast reception converter; figure 17, which is a diagram that shows the relationship between the reflection mirror and the satellite broadcast reception converter) is connected to the reflection mirror (primary reflector) (figure 17), and

a second circuit board (auxiliary board) (figure 10) of which another end portion thereof is connected to the output terminal via a lead wire and another end portion (The Examiner takes notice that the second circuit board is inherently connected to the output terminal by a wire),

wherein the first circuit board 6 (figure 10) and the second circuit board 7 (auxiliary board) (figure 10) are connected together, and laid in a contiguous sequence in the shield case 5 (chassis) (figures 1 and 2) in such a manner that the circuit board is laid closer to the primary reflector and the auxiliary board is laid closer to the output terminal (figure 10), and

circuit board 6 (paragraph 58; figure 10) and the second circuit board 7 (auxiliary board) (figure 10) are laid in such a way that said one end portion of the circuit board and said other end portion of the auxiliary board are adjacent to but separated from each other (figure 10).

Nakagawa et al further disclose each of the first and second circuit boards is soldered to the shield case by using solder filled in each recess portion (paragraph 58). Neither grounding

Art Unit: 2618

the circuit boards nor soldering the short caps to the earth pattern of the first circuit board correspond to connecting the circuit board and the auxiliary board together electrically through solder filled in a through hole.

Ikeda discloses a housing 1 which has a rectangular U-shaped top wall 1e that forms an edge of the periphery thereof (paragraph 26, figure 1), which comprises the rectangular circuit board 9 (paragraph 33, figure 5).

Ikeda only discloses the housing 1 has a rectangular U-shaped top wall 1e that forms an edge of the periphery thereof which comprises the rectangular circuit board 9. Ikeda fails to disclose that the top wall is a U-shaped pin that can be used to connect together the end portions of the circuits of Nakagawa et al. Ikeda also fails to disclose that the top wall of housing 1 is used to connect together the circuit boards contained within the housing.

These teachings clearly differ from the claimed invention, therefore, claims 3-4, 6-7, 10, and 12-14 of the present application are considered novel and nonobvious over the prior art and, consequently, are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Bobbak Safaipoor whose telephone number is (571) 270-1092. The Examiner can normally be reached on Monday-Friday from 9:00am to 5:00pm.

Art Unit: 2618

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Edan Orgad can be reached on (571) 272-7884. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

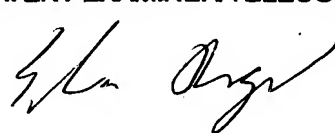
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free) or 703-305-3028.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist/customer service whose telephone number is (571) 272-2600.


Bobbok Safaipoor
B.S./bs

January 4, 2007

EDAN ORGAD
PATENT EXAMINER/TELECOMM.

 1/4/07